

N-Channel 150V MOSFET

Product summary

V_{DS} (V)	$R_{DS(on),max}$ (m Ω)	I_D (A)
150	7.5 @ $V_{GS} = 10V$	127 ⁽¹⁾

Features

- Low $R_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

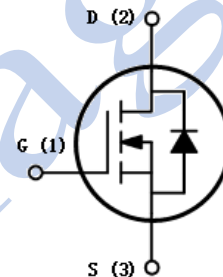
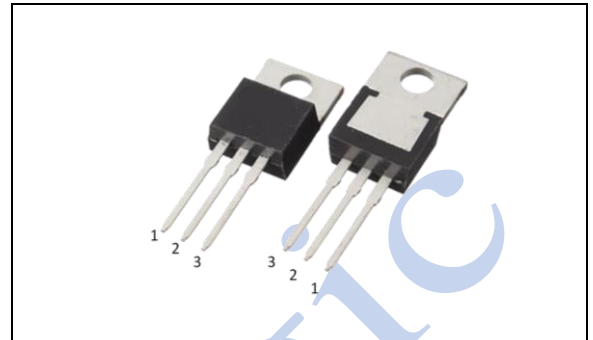
Applications

- DC/DC conversion
- Power switch
- Motor drives

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Package and ordering information

Ordering code	Package	Device code	Tube size (L*W*H)	Quantity
SDH15N7P5S1A	TO220-3L	AEP	537*33*7mm	50

TO220-3L


RoHS
COMPLIANT
HALOGEN
FREE



SDXXX
Device code
Silicon Magic discrete device

XXXX
Wafer lot number
Work week code
Year code

1. Maximum ratings

Absolute maximum ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-source voltage	V_{DS}	150	V
Gate-source voltage	V_{GS}	± 20	
Continuous drain current	I_D	127	A
		90	
		14	
Pulsed drain current ⁽²⁾	$I_{D,pulse}$	508	
Avalanche energy, single pulse ⁽³⁾	E_{AS}	720	mJ
Power dissipation	P_D	291	W
		3.7	
Operating junction and storage temperature range	T_J, T_{stg}	-55 to 175	$^\circ\text{C}$

2. Thermal resistance ratings

Thermal resistance ratings						
Parameter		Symbol	Max.	Unit		
Thermal resistance, junction-to-case	Steady state	$R_{\theta JC}$	0.52	°C/W		
Thermal resistance, junction-to-ambient ⁽⁴⁾	Steady state	$R_{\theta JA}$	40			

3. Electrical Characteristics

Electrical characteristics						
Parameter	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Static parameter						
Drain to source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	150			V
Gate-source threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2.2	2.7	3.8	V
Gate-body leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 150\text{ V}, V_{GS} = 0\text{ V}$			1	μA
Drain-source on-resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 100\text{ A}$		6.3	7.5	m Ω
Forward transconductance ⁽⁵⁾	g_{fs}	$V_{DS} = 5\text{ V}, I_D = 100\text{ A}$		175		S
Gate resistance	R_g	$f = 1\text{ MHz}$		2		Ω
Dynamic ⁽⁵⁾						
Total gate charge	Q_g	$V_{DS} = 75\text{ V}, I_D = 100\text{ A}, V_{GS} = 10\text{ V}$		144		nC
Gate-source charge	Q_{gs}			37		
Gate-drain charge	Q_{gd}			47		
Turn-on delay time	$t_{d(on)}$	$V_{DS} = 75\text{ V}, I_D = 100\text{ A}, V_{GS} = 10\text{ V},$ $R_{GEN} = 6\text{ }\Omega$		63		ns
Rise time	t_r			67		
Turn-off delay time	$t_{d(off)}$			135		
Fall time	t_f			51		
Input capacitance	C_{iss}	$V_{DS} = 75\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		8090		pF
Output capacitance	C_{oss}			533		
Reverse transfer capacitance	C_{rss}			31		
Reverse Diode Characteristics ⁽⁵⁾						
Diode forward voltage	V_{SD}	$V_{GS} = 0\text{ V}, I_F = 100\text{ A}$		1	1.2	V
Reverse recovery time	t_{rr}	$V_{DS} = 75\text{ V}, I_F = 100\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		117		ns
Reverse recovery charge	Q_{rr}			446		nC

Notes

- (1) Limited by maximum junction temperature.
- (2) Pulse width limited by maximum junction temperature.
- (3) $V_{DS} = 75\text{ V}, V_{GS} = 10\text{ V}, L = 0.3\text{ mH}$.
- (4) $R_{\theta JA}$ is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5x1.5 in. board of FR-4 material.
- (5) Guaranteed by design, not subject to production testing.

4. Electrical characteristics diagrams

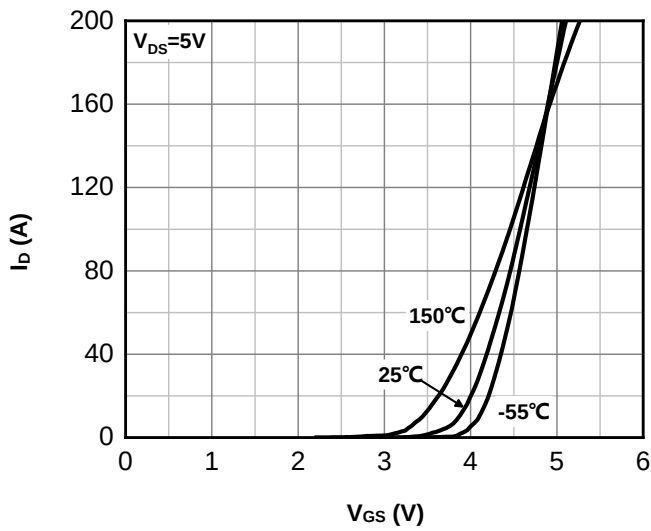
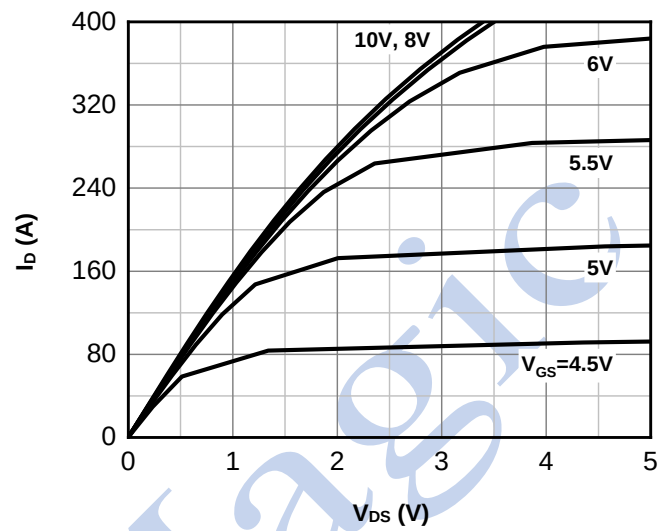
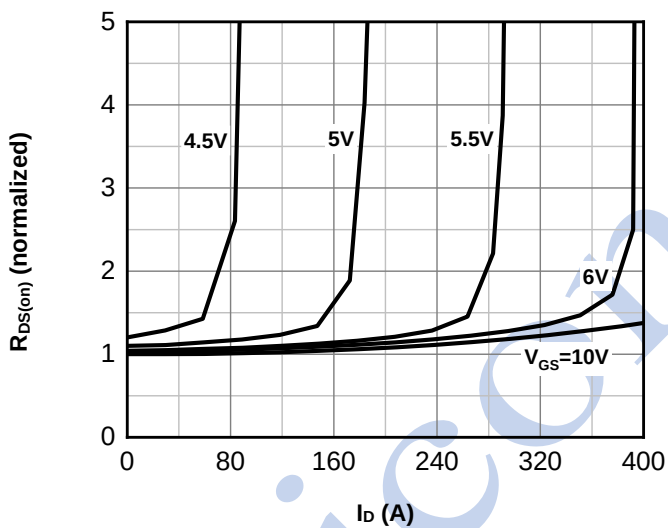
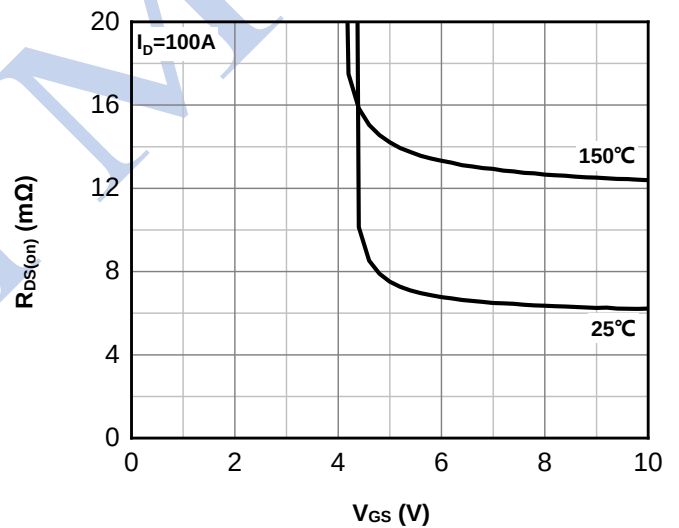
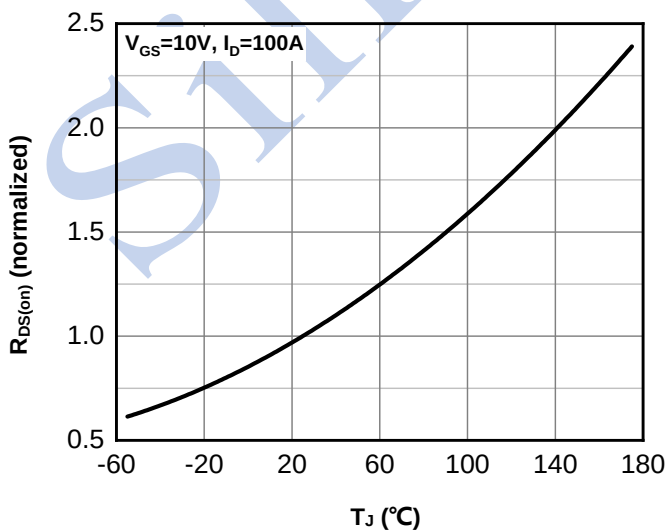
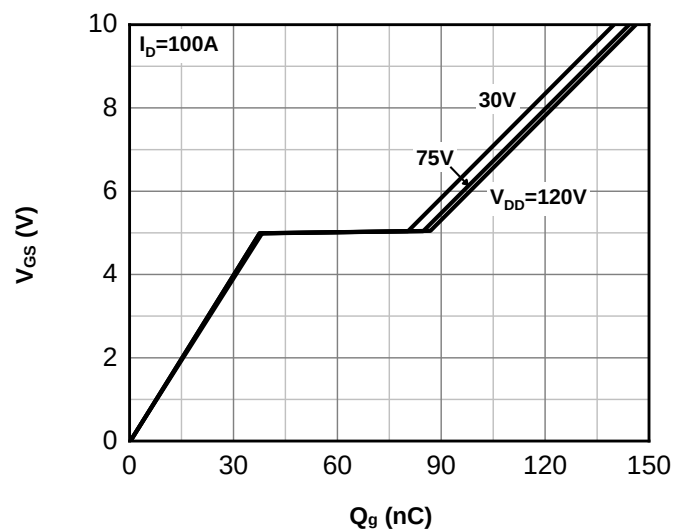
Fig.1 Typ. transfer characteristics

Fig.2 Typ. output characteristics

Fig.3 Normalized on-resistance vs drain current

Fig.4 Typ. on-resistance vs gate-source voltage

Fig.5 Normalized on-resistance vs junction temperature

Fig.6 Typ. gate charge


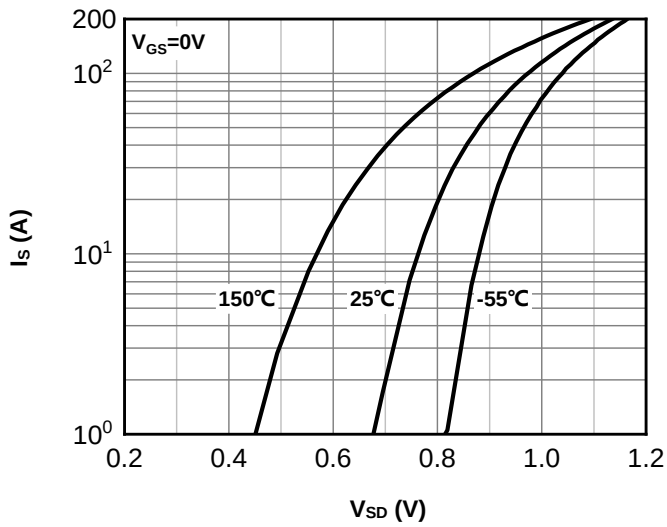
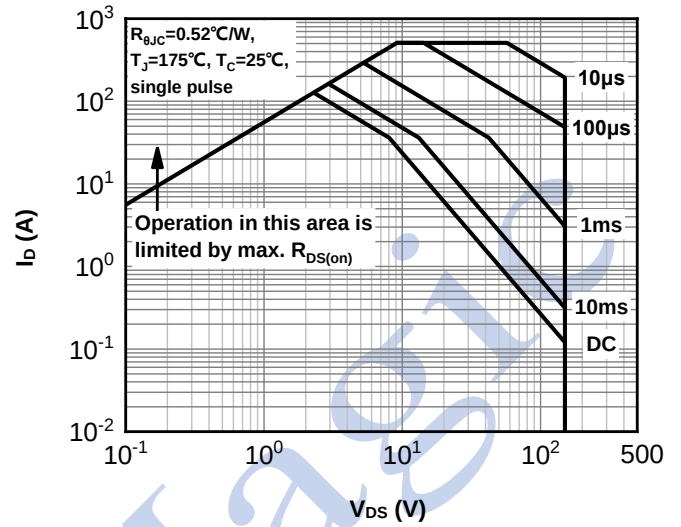
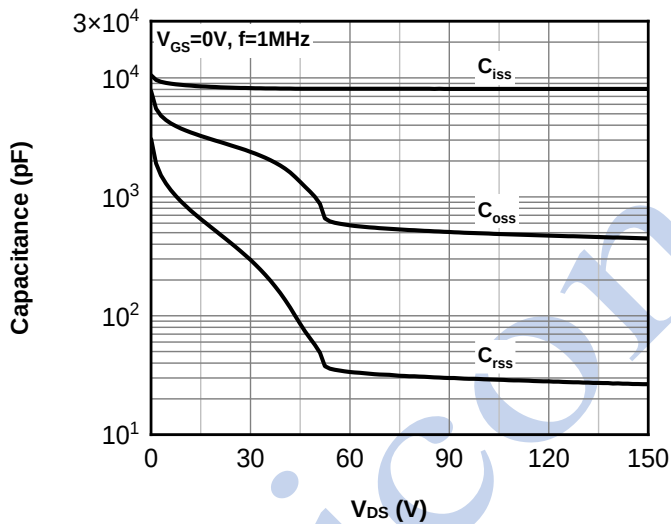
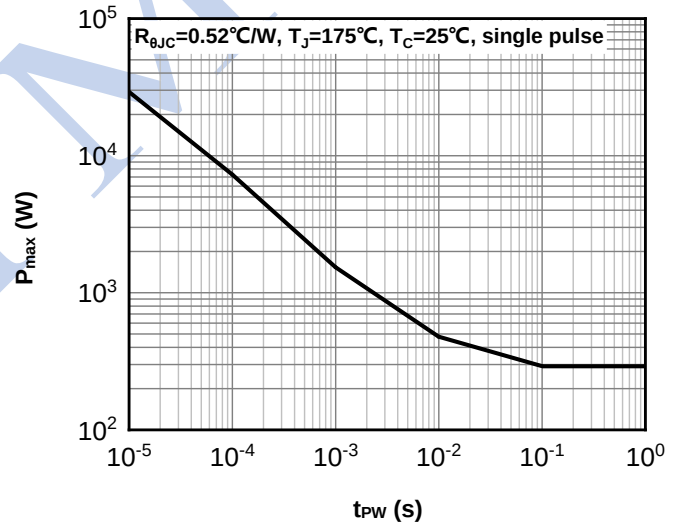
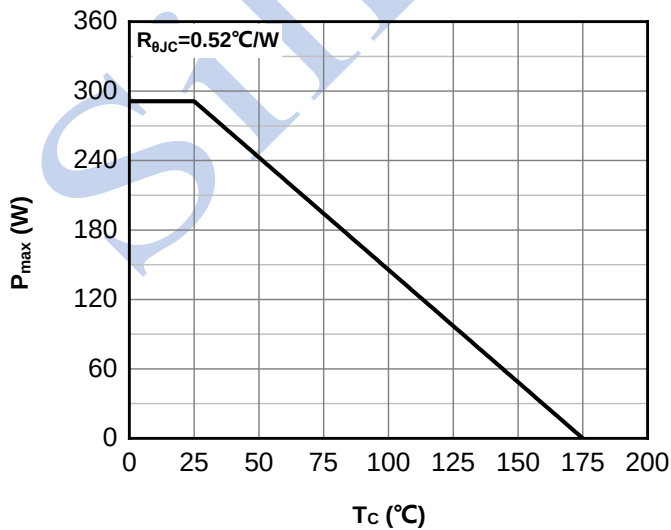
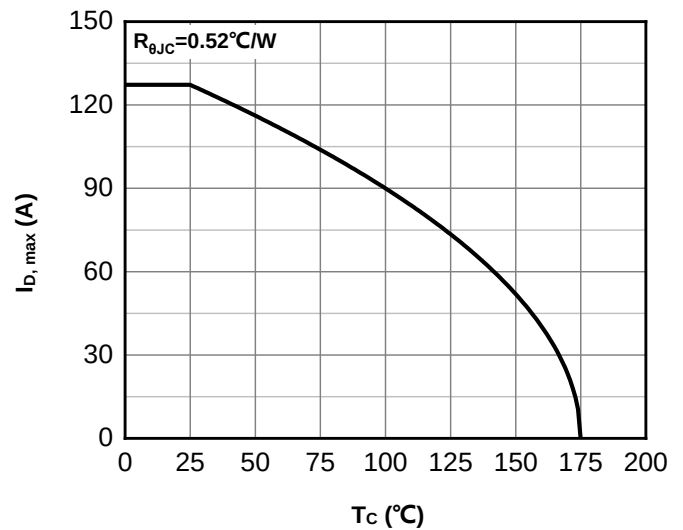
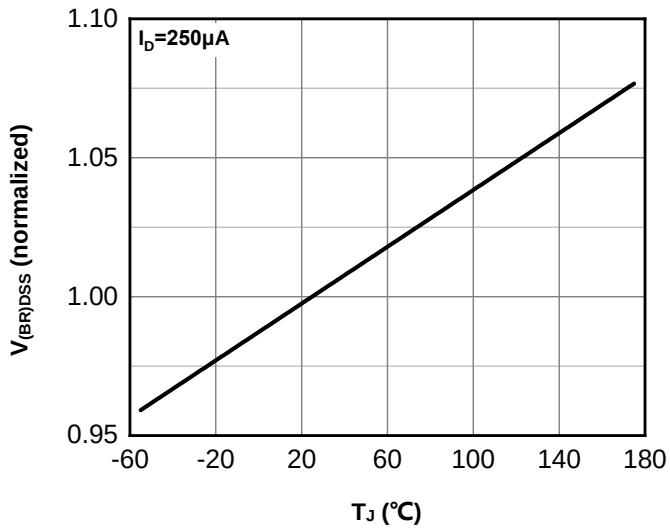
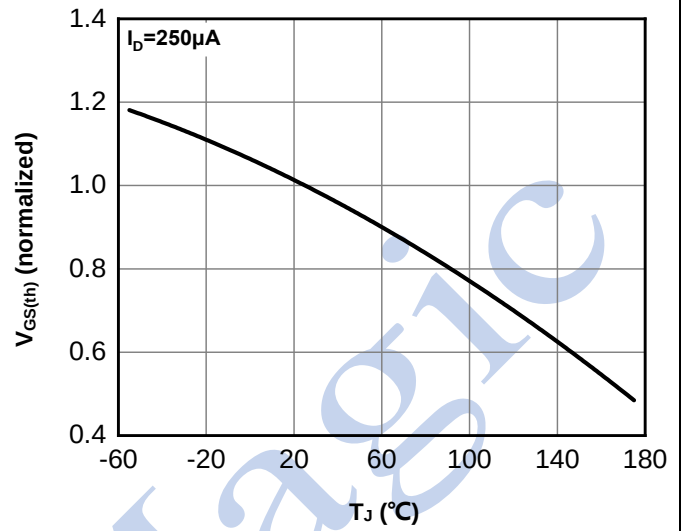
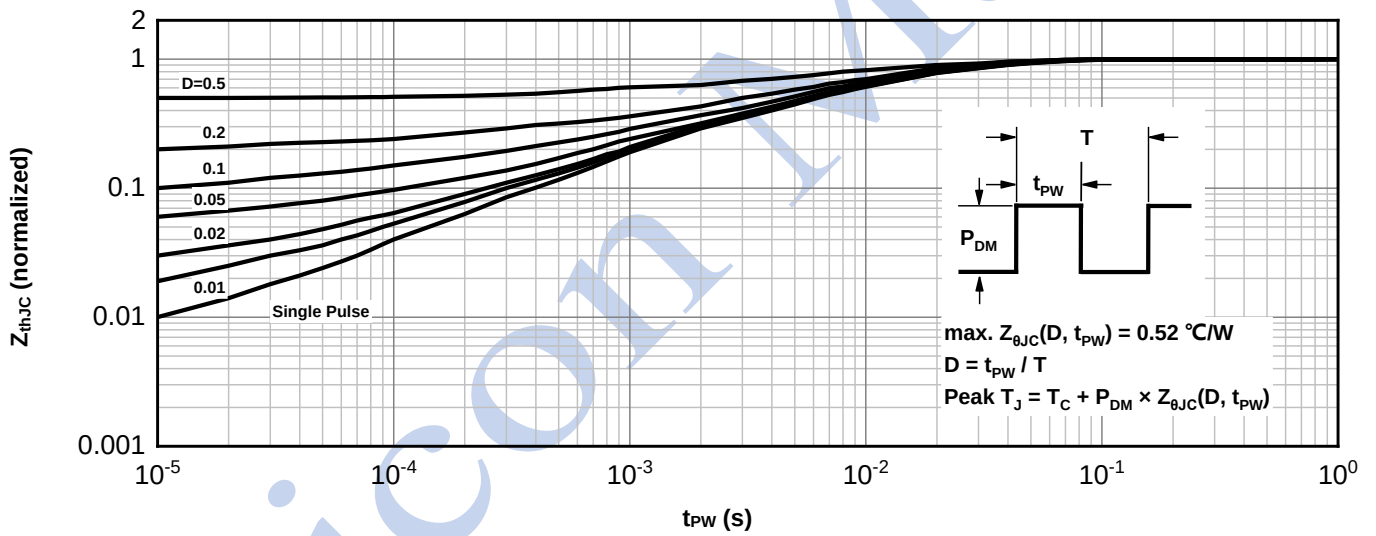
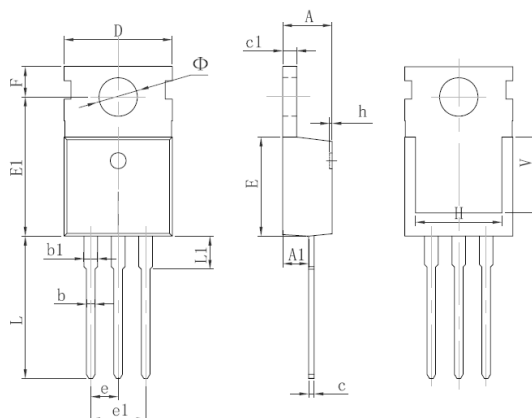
Fig.7 Typ. forward characteristics of body diode

Fig.8 Safe operating area

Fig.9 Typ. Capacitance

Fig.10 Single pulse maximum power dissipation

Fig.11 Max. power dissipation vs case temperature

Fig.12 Max. continuous drain current vs case temperature


Fig.13 Normalized $V_{(BR)DSS}$ vs junction temperature

Fig.14 Normalized $V_{GS(th)}$ vs junction temperature

Fig.15 Normalized transient thermal impedance from junction to case


5. Package outline dimensions



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.40	4.60
A1	2.25	2.55
b	0.71	0.91
b1	1.17	1.37
c	0.33	0.65
c1	1.20	1.40
D	9.91	10.25
E	8.95	9.75
E1	12.65	13.05
e	2.54 TYP.	
e1	4.98	5.18
F	2.65	2.95
H	7.90	8.10
h	0.00	0.30
L	12.90	13.40
L1	2.85	3.25
V	6.90 REF.	
Φ	3.40	3.80

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