

N-Channel 40V MOSFET

Product summary

V _{DS} (V)	$R_{DS(on),max}$ (m Ω)	I _D (A)
40	7.2 @ V _{GS} = 10V	23 (1)

Features

- For automotive applications and AEC-Q101 qualified
- Low FOM and thermal impedance
- Fast switching speed
- 100% avalanche tested

Applications

- DC/DC conversion
- Power switch
- Motor drives

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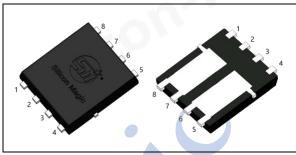
Package and ordering information

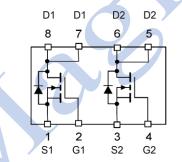
Ordering code	Package	Device code
SDA04K006N-AA	PDFN5*6-D	AKS

1. Maximum ratings

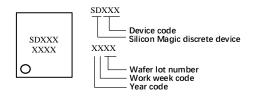
Absolute maximum ratings (T _A = 25℃ unless otherwise noted)					
Parameter			Limit	Unit	
Drain-source voltage			40	V	
Gate-source voltage		V_{GS}	±20	V	
	T _C =25°C ⁽¹⁾		23		
Continuous drain current	Tc=100°C	I _D	23	А	
	T _A =25°C ⁽⁴⁾		13		
Pulsed drain current ⁽²⁾			92		
Avalanche energy, single pulse ⁽³⁾		E _{AS}	20	mJ	
Dower discination	T _C =25℃	P_{D}	25	W	
Power dissipation	T _A =25°C ⁽⁴⁾	' D	2.1		
Operating junction and storage temperature range		T_J,T_stg	-55 to 175	$^{\circ}$	

PDFN5*6-D













2. Thermal resistance ratings

Thermal resistance ratings				
Parameter	Symbol	Max.	Unit	
Thermal resistance, junction-to-case	Steady state	Rejc	6	°C/W
Thermal resistance, junction-to-ambient (4)	Steady state	Reja	70	C/VV

3. Electrical Characteristics

Electrical characteristics (T _J = 25°C unless otherwise noted)							
Parameter	Symbol	Test conditions	Min.	Тур.	Max.	Unit	
Static parameter							
Drain to source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}$	40			٧	
Gate-source threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1.2	1.8	2.4	٧	
Gate-body leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V	7		±100	nA	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V			1	μA	
Drain-source on-resistance	R _{DS(on)}	V _{GS} = 10 V, I _D = 10 A		6	7.2	7.2 12 mΩ	
Dialii-source diffesistance	T CDS(on)	V _{GS} = 4.5 V, I _D = 5 A		8.8	12		
Forward transconductance ⁽⁵⁾	g_{fs}	$V_{DS} = 5 \text{ V}, I_{D} = 20 \text{ A}$		30		S	
Gate resistance	Rg	f = 1 MHz		2.5		Ω	
Dynamic ⁽⁵⁾							
Total gate charge	Qg	$V_{DS} = 20 \text{ V}, I_{D} = 5 \text{ A}, V_{GS} = 4.5 \text{ V}$		8			
Total gate charge	Q_g			15		nC	
Gate-source charge	Q_{gs}	$V_{DS} = 20 \text{ V}, I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}$		2.8		nc	
Gate-drain charge	Q_{gd}			3.7			
Turn-on delay time	t _{d(on)}			11			
Rise time	tr	V _{DS} = 20 V, I _D =10 A, V _{GS} = 10 V,		14			
Turn-off delay time	t _{d(off)}	R _{GEN} = 6 Ω		20		ns	
Fall time	tf			14			
Input capacitance	C _{iss}			910			
Output capacitance	C _{oss}	V _{DS} = 20 V, V _{GS} = 0 V, f = 1 MHz		390		pF	
Reverse transfer capacitance	C _{rss}			18			
Reverse Diode Characteristics ⁽⁵⁾							
Diode forward voltage	V _{SD}	V _{GS} = 0 V, I _F = 10 A		0.8	1.1	V	
Reverse recovery time	t _{rr}	V/- 20 V I = 40 A 49/45 = 400 A/9		28		ns	
Reverse recovery charge	Qrr	V _{DS} = 20 V, I _F = 10 A, di/dt = 100 A/μs		14		nC	

Notes

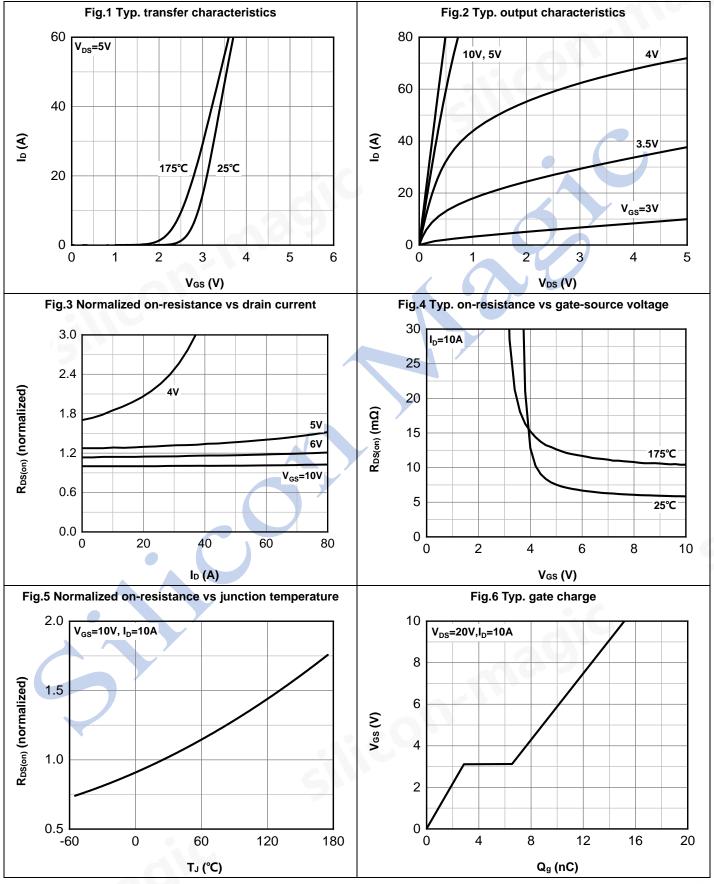
- (1) Package Limited.
- (2) Pulse width limited by Package.
- (3) $V_{DS} = 20 \text{ V}$, $V_{GS} = 10 \text{ V}$, L = 0.3 mH.
- (4) R_{BJA} is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5x1.5 in. board of FR-4 material.
- (5) Guaranteed by design, not subject to production testing.



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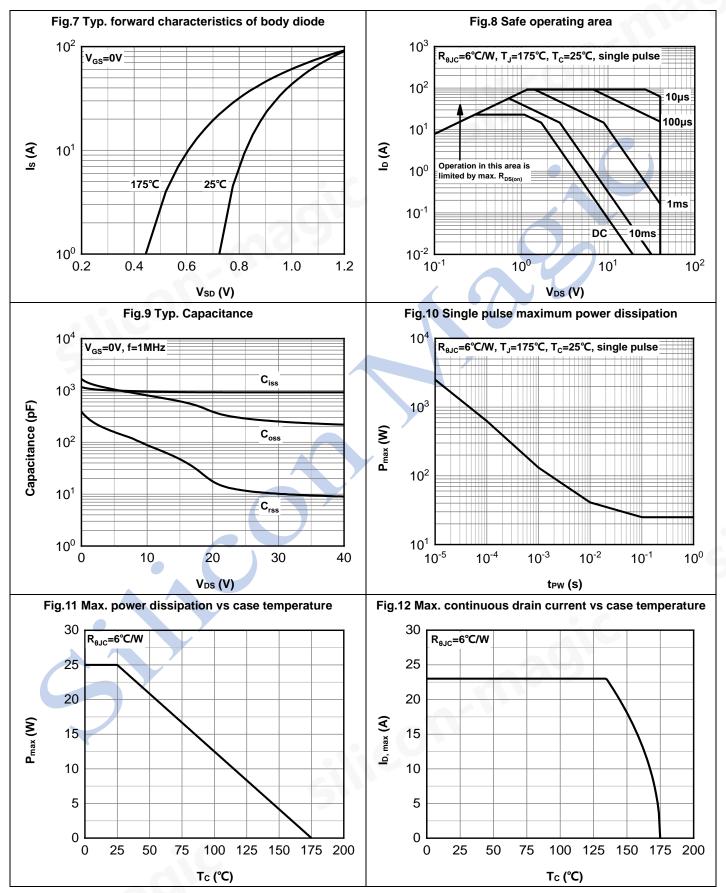


4. Electrical characteristics diagrams



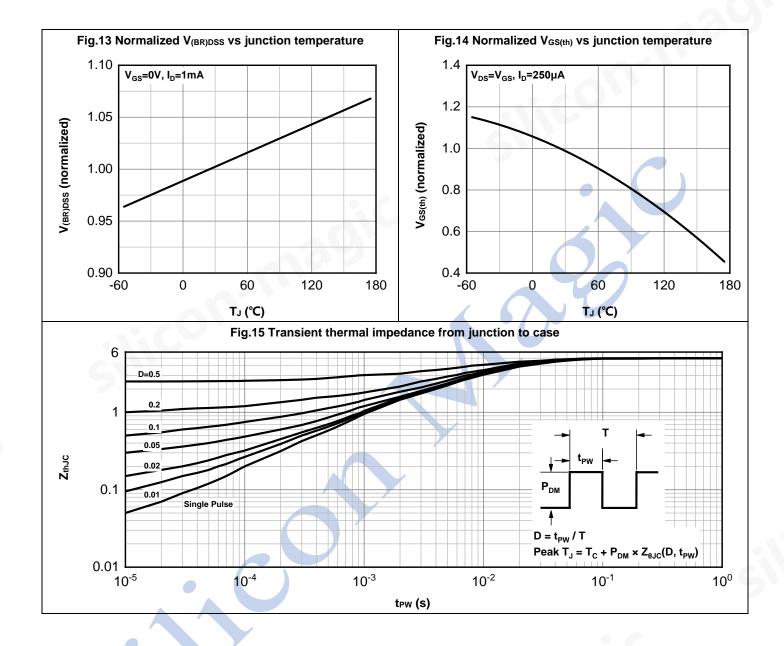






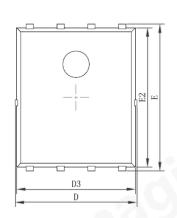


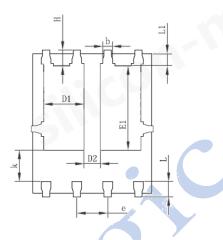


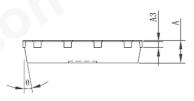




5. Package outline dimensions







Dim	Millimeters			
	Min	Nom	Max	
А	0.900	-	1.000	
А3		0.254 REF		
D	4.944	ı	5.096	
Е	5.974	ı	6.126	
D1	1.470	ı	1.870	
D2	0.470	1	0.870	
E1	3.375	1	3.575	
D3	4.824	1	4.976	
E2	5.674	1	5.826	
k	1.190	1	1.390	
b	0.350	1	0.450	
е	1.270 TYP			
L	0.559	1	0.711	
L1	0.424		0.576	
Н	0.574		0.726	
θ	10°	-	12°	





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