

SDN06K9P5C-AA

N-Channel 65 V MOSFET

Product summary

V _{DS} (V)	$R_{DS(on),max}$ (m Ω)	I _D (A)
65	9.5 @ V _{GS} = 10V	45 ⁽¹⁾

Features

- Low RDS(on)
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

Applications

- DC/DC conversion
- Power switch
- Motor drives

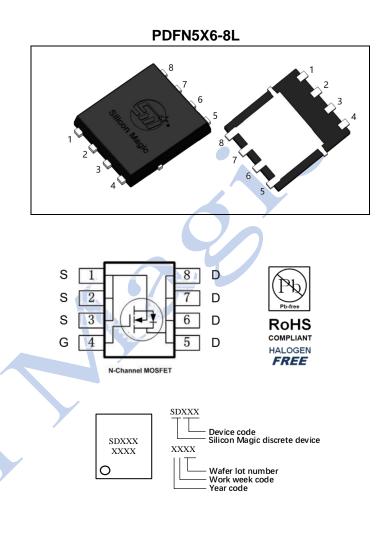
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Package and ordering information

Ordering code	Package	Device of	code
SDN06K9P5C-AA	PDFN5X6-8L	ALQ	

1. Maximum ratings

Absolute maximum ratings ($T_A = 25^{\circ}C$ unless otherwise noted)					
Parameter			Limit	Unit	
Drain-source voltage			65	V	
Gate-source voltage			±20		
	$T_{C}=25^{\circ}C^{(1)}$		45		
Continuous drain current	Tc=100℃	I _D	28	А	
	T _A =25°C ⁽⁴⁾		15	A	
Pulsed drain current ⁽²⁾			179		
Avalanche energy, single pulse ⁽³⁾		E _{AS}	20	mJ	
Dewerdesignetion	T _C =25℃	P _D	35.7	W	
Power dissipation	T _A =25°C ⁽⁴⁾	' D	2.7	vv	
Operating junction and storage temperature range			-55 to 150	°C	







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2. Thermal resistance ratings

Thermal resistance ratings					
Parameter	Symbol	Max.	Unit		
Thermal resistance, junction-to-case	Steady state	Rejc	3.5	°C/W	
Thermal resistance, junction-to-ambient (4)	Steady state	Reja	45	0/00	

3. Electrical Characteristics

Electrical characteristics (TJ = 25℃ unless otherwise noted)							
Parameter	Symbol	Test conditions	Min.	Тур.	Max.	Unit	
Static parameter							
Drain to source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0, I _D = 250 μA	65			V	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.2	1.9	2.6	V	
Gate-body leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			±100	nA	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V			1	μA	
	P	V _{GS} = 10 V, I _D = 20 A		7.5	9.5	-	
Drain-source on-resistance	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$		13	16	mΩ 16	
Forward transconductance ⁽⁵⁾	g _{fs}	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		40		S	
Gate resistance	Rg	f = 1MHz		2.5		Ω	
Dynamic ⁽⁵⁾							
Total gate charge	Qg	$V_{DS} = 30 \text{ V}, \text{ I}_{D} = 10 \text{ A}, \text{ V}_{GS} = 4.5 \text{ V}$		11			
Total gate charge	Q _g			19.5		-0	
Gate-source charge	Q _{gs}	V _{DS} = 30 V, I _D = 20 A, V _{GS} = 10 V		3.5		nC	
Gate-drain charge	Q _{gd}			7			
Turn-on delay time	t _{d(on)}			15			
Rise time	tr	V _{DS} = 30 V, I _D = 20 A, V _{GS} = 10 V,		40			
Turn-off delay time	t _{d(off)}	$R_{GEN} = 6 \Omega$		38		ns	
Fall time	t _f			15			
Input capacitance	C _{iss}			895			
Output capacitance	C _{oss}	V _{DS} = 30 V, V _{GS} = 0 V, f = 1 MHz		300		pF	
Reverse transfer capacitance	C _{rss}			25			
Reverse Diode Characteristics ⁽⁵⁾							
Diode forward voltage	V _{SD}	$V_{GS} = 0 V, I_F = 20 A$		0.9	1.2	V	
Reverse recovery time	trr			28		ns	
Reverse recovery charge	Qrr	V _{DS} = 30 V, I _F = 20 A, di/dt = 100 A/µs		15		nC	

Notes

(1) Limited by maximum junction temperature.

(2) Pulse width limited by maximum junction temperature.

(3) $V_{DS} = 30 V$, $V_{GS} = 10 V$, L = 0.3 mH.

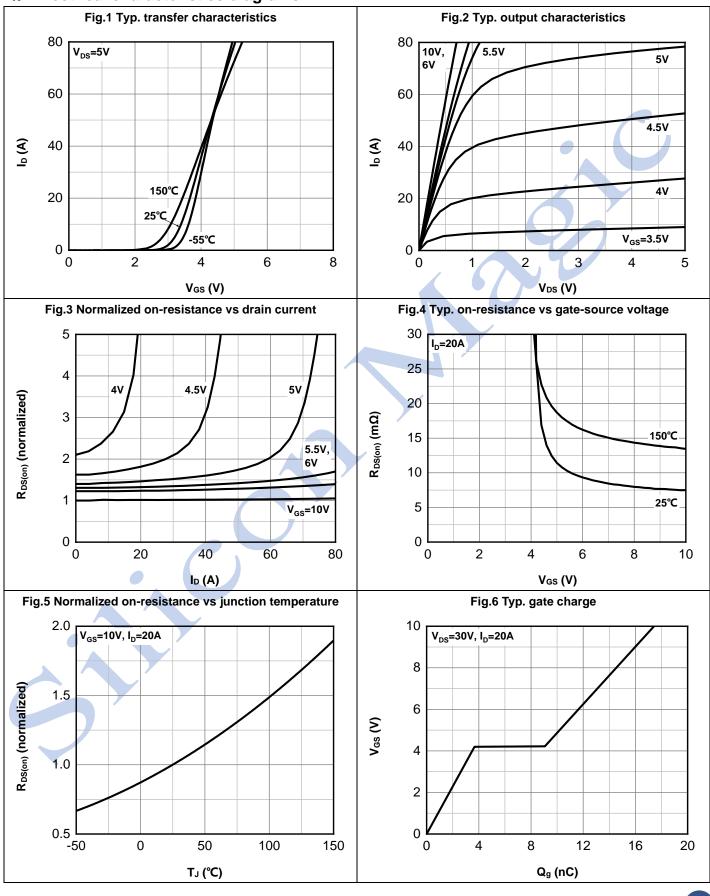
(4) $R_{\theta JA}$ is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5x1.5 in. board of FR-4 material.

(5) Guaranteed by design, not subject to production testing.





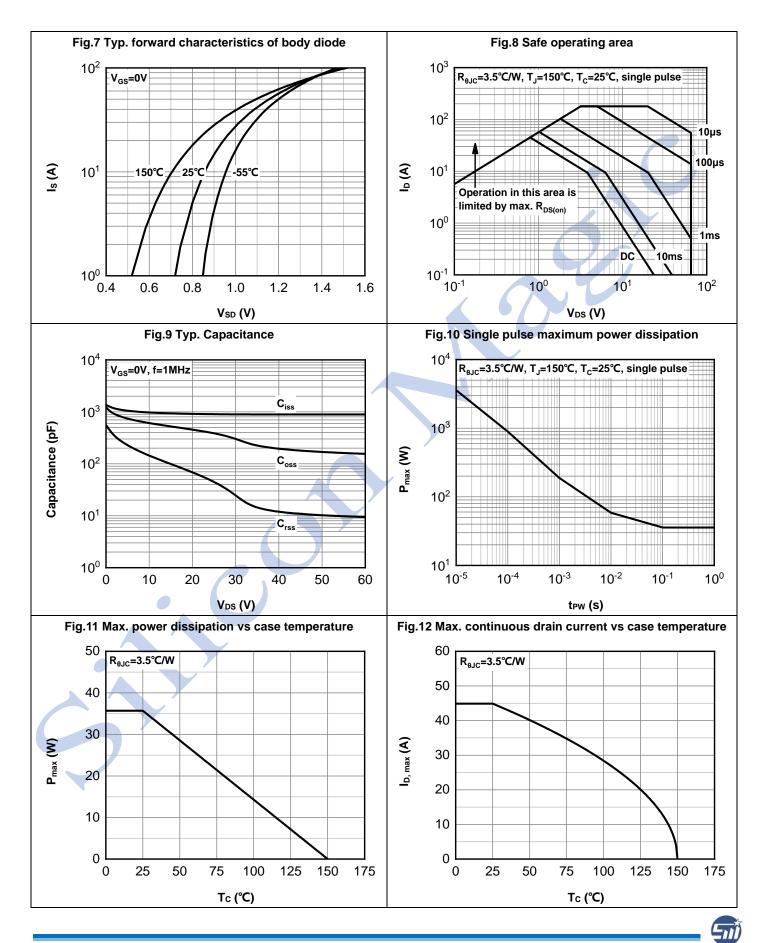
4. Electrical characteristics diagrams



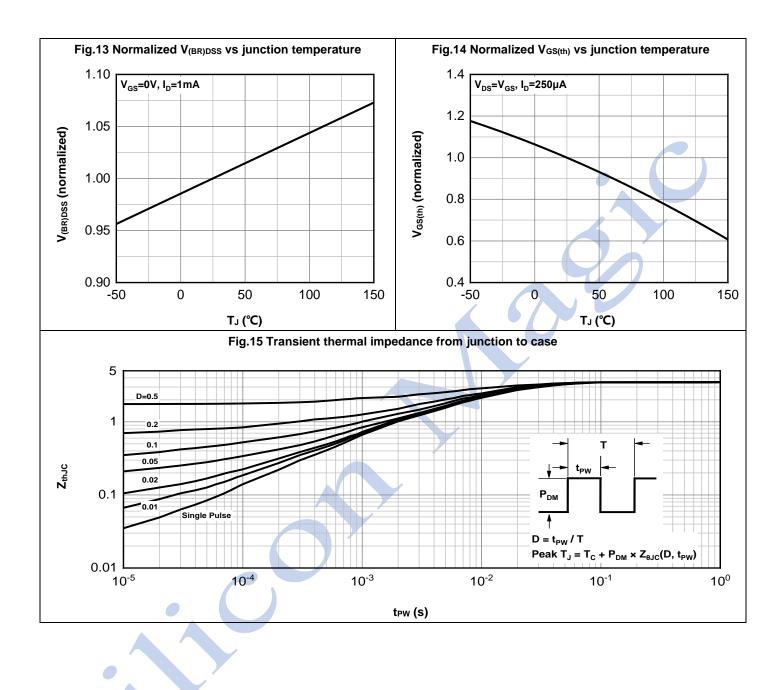
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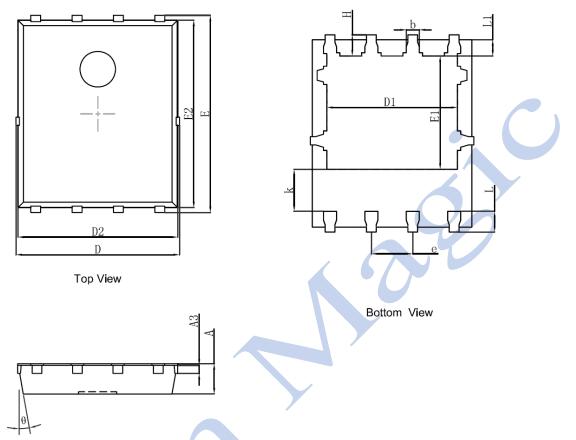






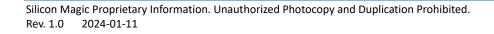


5. Package outline dimensions



Side View

Dim	Millimeters			
Dim	Min	Nom	Max	
А	0.900	-	1.000	
A3		0.254 REF		
D	4.944	-	5.096	
E	5.974	-	6.126	
D1	3.910	-	4.110	
E1	3.375	-	3.575	
D2	4.824	-	4.976	
E2	5.674	-	5.826	
k	1.190	-	1.390	
b	0.350	-	0.450	
е	1.270 TYP			
L	0.559	-	0.711	
L1	0.424	-	0.576	
Н	0.574	-	0.726	
θ	10°	-	12°	





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