

# N-Channel 110V MOSFET

## Product summary


| $V_{DS}$ (V) | $R_{DS(on),max}$ (m $\Omega$ ) | $I_D$ (A)          |
|--------------|--------------------------------|--------------------|
| 110          | 4.3 @ $V_{GS} = 10V$           | 155 <sup>(1)</sup> |

## Features

- Low  $R_{DS(on)}$  SGT technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

## Applications

- DC/DC conversion
- Power switch
- Motor drives

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## Package and ordering information

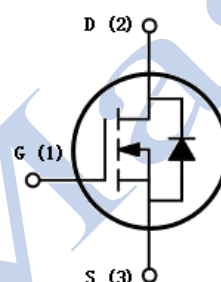
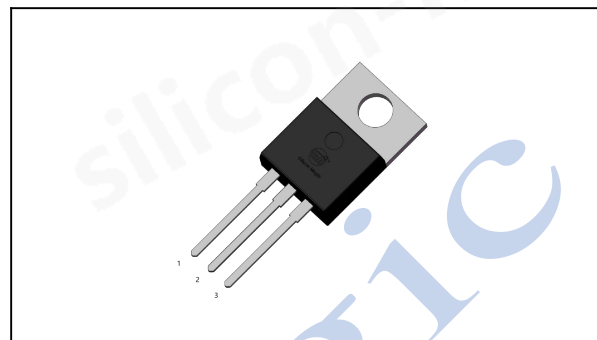
| Ordering code | Package  | Device code |
|---------------|----------|-------------|
| SDN11N4P3A-AA | TO220-3L | ALH         |

## 1. Maximum ratings

Absolute maximum ratings ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

| Parameter  |   | Symbol         | Limit      | Unit             |
|--|---|----------------|------------|------------------|
| Drain-source voltage                             |   | $V_{DS}$       | 110        | V                |
| Gate-source voltage                              |   | $V_{GS}$       | $\pm 20$   |                  |
| Continuous drain current                         | $T_C = 25^\circ\text{C}$ <sup>(1)</sup> | $I_D$          | 155        | A                |
|  | $T_C = 100^\circ\text{C}$               |                | 102        |                  |
|  | $T_A = 25^\circ\text{C}$ <sup>(4)</sup> |                | 20         |                  |
| Pulsed drain current <sup>(2)</sup>              |   | $I_{D,pulse}$  | 620        |                  |
| Avalanche energy, single pulse <sup>(3)</sup>    |   | $E_{AS}$       | 180        | mJ               |
| Power dissipation                                | $T_C = 25^\circ\text{C}$                | $P_D$          | 198        | W                |
|  | $T_A = 25^\circ\text{C}$ <sup>(4)</sup> |                | 3.1        |                  |
| Operating junction and storage temperature range |   | $T_J, T_{stg}$ | -55 to 150 | $^\circ\text{C}$ |

**TO220-3L**



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**



SDXXX  
XXXXX

Device code  
Silicon Magic discrete device

XXXXX  
XXXXX

Wafer lot number  
Work week code  
Year code

## 2. Thermal resistance ratings

| Thermal resistance ratings                             |              |                 |      |      |
|--|--------------|-----------------|------|------|
| Parameter  |              | Symbol          | Max. | Unit |
| Thermal resistance, junction-to-case                   | Steady state | $R_{\theta JC}$ | 0.63 | °C/W |
| Thermal resistance, junction-to-ambient <sup>(4)</sup> | Steady state | $R_{\theta JA}$ | 40   |      |

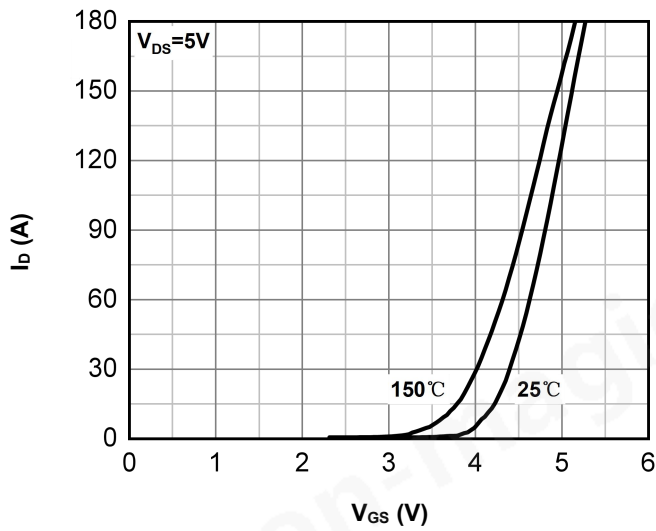
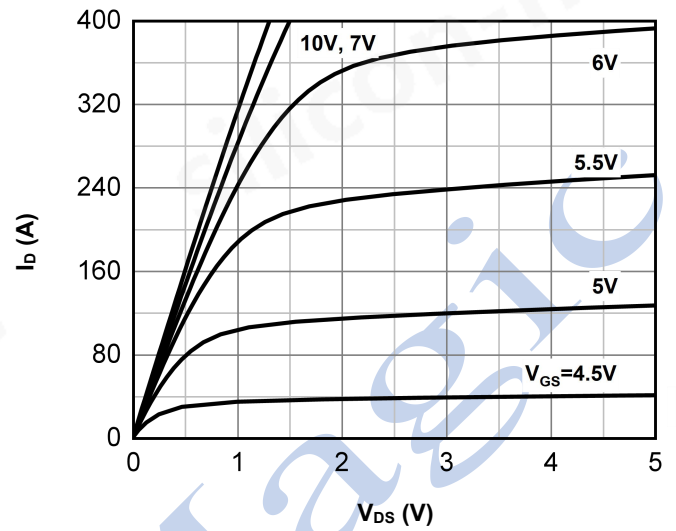
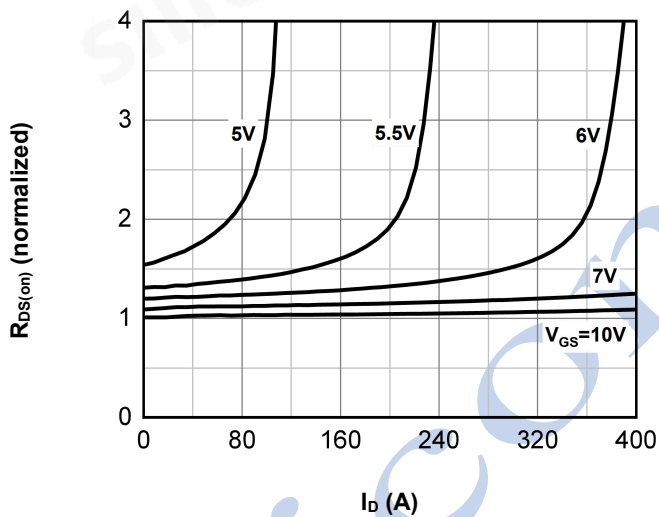
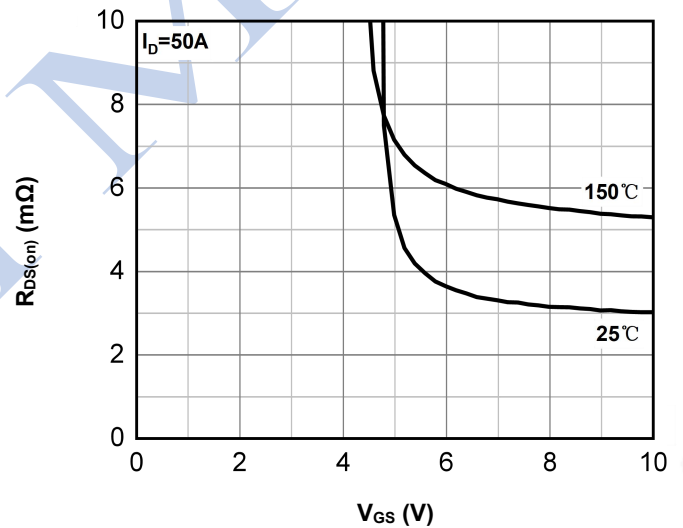
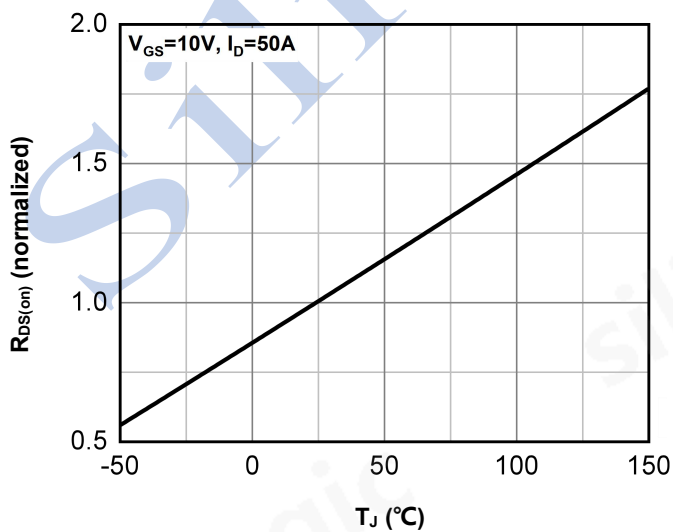
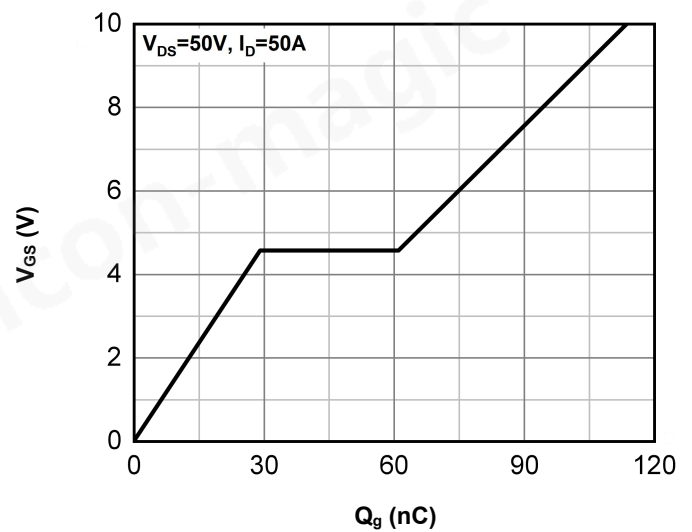
## 3. Electrical Characteristics

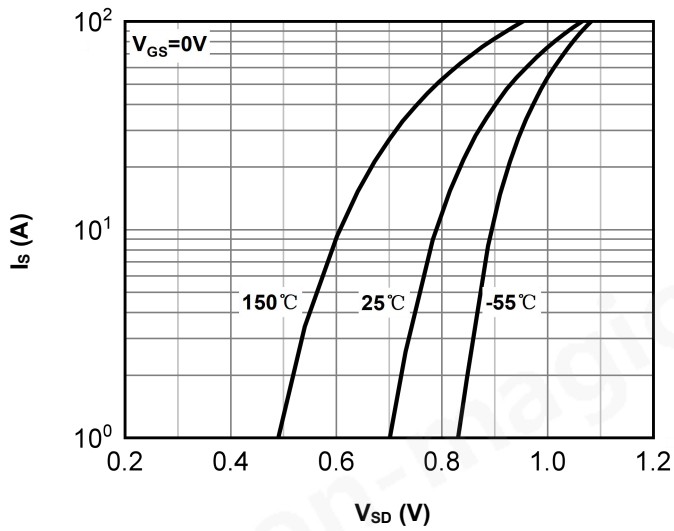
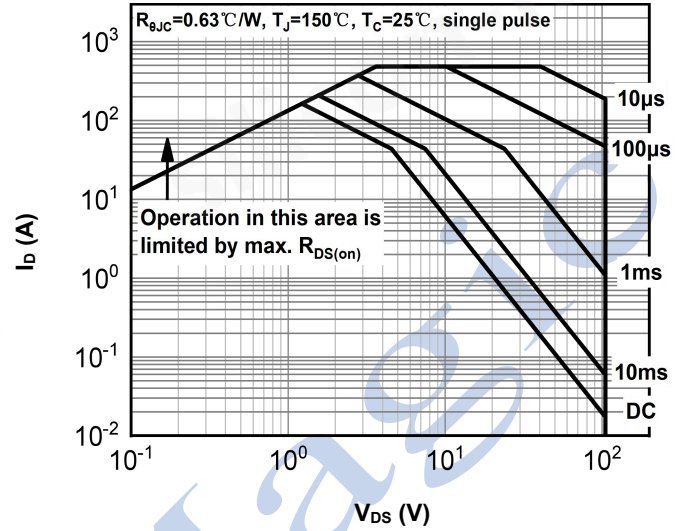
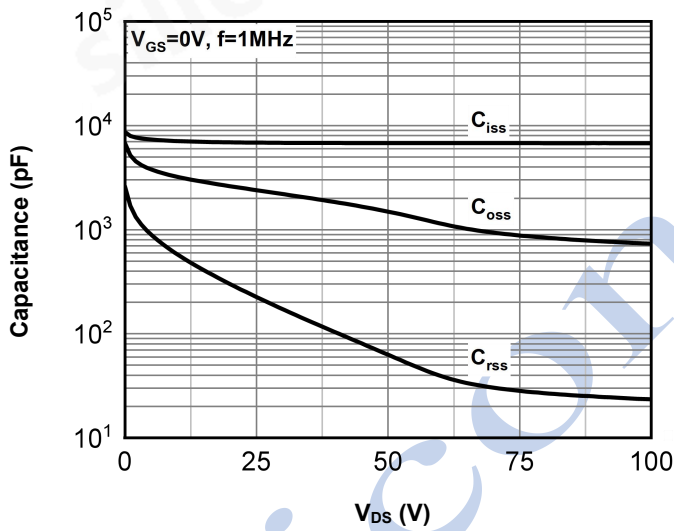
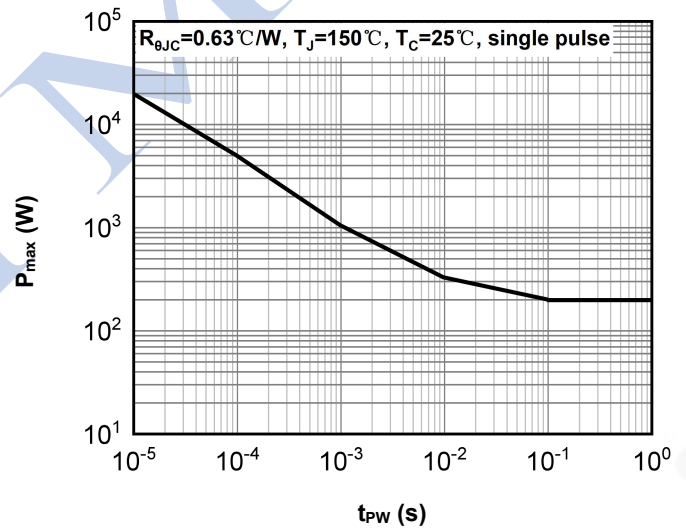
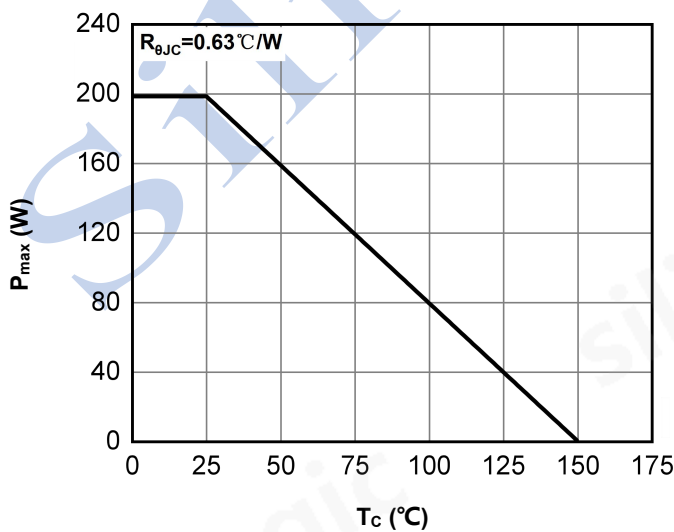
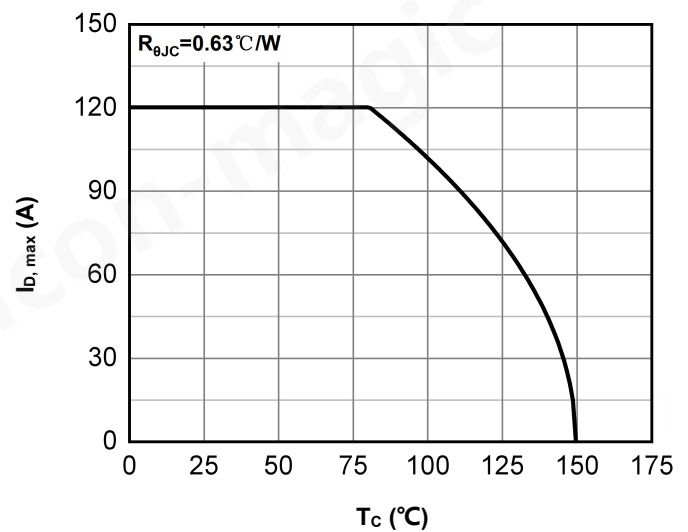
| Electrical characteristics (T <sub>J</sub> = 25°C unless otherwise noted) |                      |  |      |      |      |      |
|---|----------------------|--|------|------|------|------|
| Parameter   | Symbol               | Test conditions  | Min. | Typ. | Max. | Unit |
| Static parameter  |                      |  |      |      |      |      |
| Drain to source breakdown voltage   | V <sub>(BR)DSS</sub> | V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA   | 110  |      |      | V    |
| Gate-source threshold voltage   | V <sub>GS(th)</sub>  | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA                                      | 2.5  | 3.3  | 4.1  | V    |
| Gate-body leakage   | I <sub>GSS</sub>     | V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V   |      |      | ±100 | nA   |
| Zero gate voltage drain current   | I <sub>DSS</sub>     | V <sub>DS</sub> = 105 V, V <sub>GS</sub> = 0 V   |      |      | 1    | μA   |
| Drain-source on-resistance  | R <sub>DS(on)</sub>  | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A  |      | 3.4  | 4.3  | mΩ   |
| Forward transconductance <sup>(5)</sup>                                   | g <sub>fs</sub>      | V <sub>DS</sub> = 5 V, I <sub>D</sub> = 50 A   |      | 140  |      | S    |
| Gate resistance   | R <sub>g</sub>       | f = 1 MHz  |      | 1.2  |      | Ω    |
| Dynamic <sup>(5)</sup>  |                      |  |      |      |      |      |
| Total gate charge   | Q <sub>g</sub>       | V <sub>DS</sub> = 50 V, I <sub>D</sub> = 50 A, V <sub>GS</sub> = 10 V                            |      | 114  |      | nC   |
| Gate-source charge  | Q <sub>gs</sub>      |  |      | 29   |      |      |
| Gate-drain charge   | Q <sub>gd</sub>      |  |      | 32   |      |      |
| Turn-on delay time  | t <sub>d(on)</sub>   | V <sub>DS</sub> = 50 V, I <sub>D</sub> = 50 A, V <sub>GS</sub> = 10 V,<br>R <sub>GEN</sub> = 6 Ω |      | 58   |      | ns   |
| Rise time   | t <sub>r</sub>       |  |      | 83   |      |      |
| Turn-off delay time   | t <sub>d(off)</sub>  |  |      | 76   |      |      |
| Fall time   | t <sub>f</sub>       |  |      | 70   |      |      |
| Input capacitance   | C <sub>iss</sub>     | V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V, f = 1 MHz   |      | 6750 |      | pF   |
| Output capacitance  | C <sub>oss</sub>     |  |      | 1480 |      |      |
| Reverse transfer capacitance  | C <sub>rss</sub>     |  |      | 61   |      |      |
| Reverse Diode Characteristics <sup>(5)</sup>                              |                      |  |      |      |      |      |
| Diode forward voltage   | V <sub>SD</sub>      | V <sub>GS</sub> = 0 V, I <sub>F</sub> = 50 A   |      | 0.9  | 1.2  | V    |
| Reverse recovery time   | t <sub>rr</sub>      | V <sub>DS</sub> = 50 V, I <sub>F</sub> = 50 A, di/dt = 100 A/μs                                  |      | 84   |      | ns   |
| Reverse recovery charge   | Q <sub>rr</sub>      |  |      | 218  |      | nC   |

### Notes

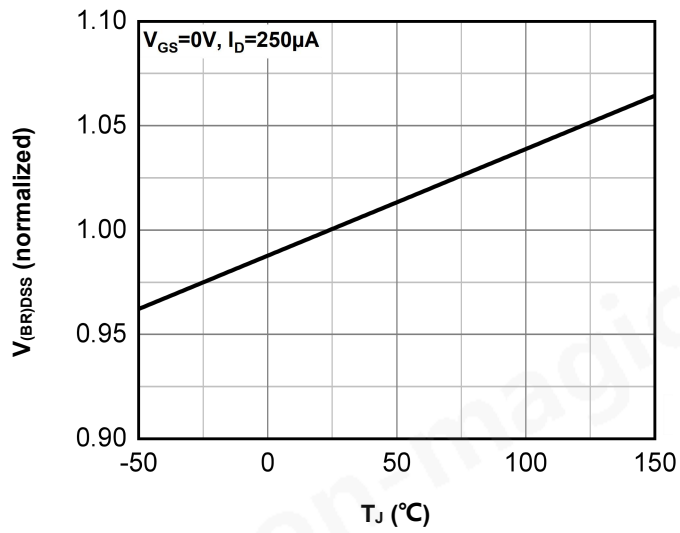
- (1) Limited by maximum junction temperature.
- (2) Pulse width limited by maximum junction temperature.
- (3)  $V_{DS} = 50\text{ V}, V_{GS} = 10\text{ V}, L = 0.1\text{ mH}$ .
- (4)  $R_{\theta JA}$  is determined with the device mounted on a 1 in<sup>2</sup> pad 2 oz copper pad on a 1.5x1.5 in. board of FR-4 material.
- (5) Guaranteed by design, not subject to production testing.

#### 4. Electrical characteristics diagrams

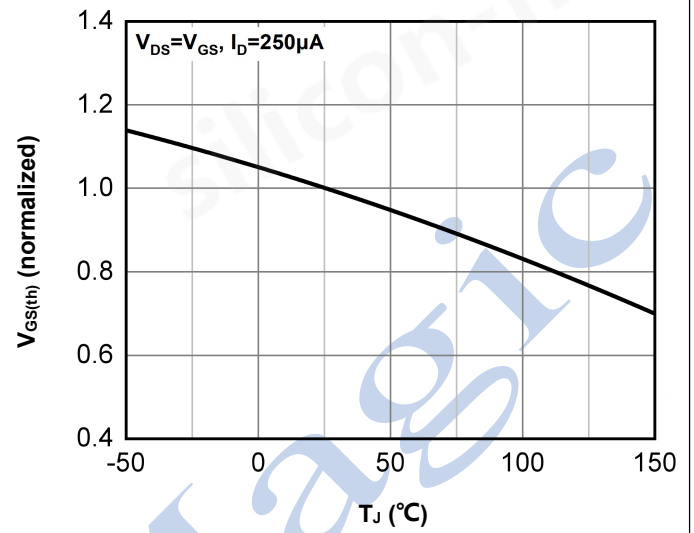
**Fig.1 Typ. transfer characteristics**

**Fig.2 Typ. output characteristics**

**Fig.3 Normalized on-resistance vs drain current**

**Fig.4 Typ. on-resistance vs gate-source voltage**

**Fig.5 Normalized on-resistance vs junction temperature**

**Fig.6 Typ. gate charge**


**Fig.7 Typ. forward characteristics of body diode**

**Fig.8 Safe operating area**

**Fig.9 Typ. Capacitance**

**Fig.10 Single pulse maximum power dissipation**

**Fig.11 Max. power dissipation vs case temperature**

**Fig.12 Max. continuous drain current vs case temperature**


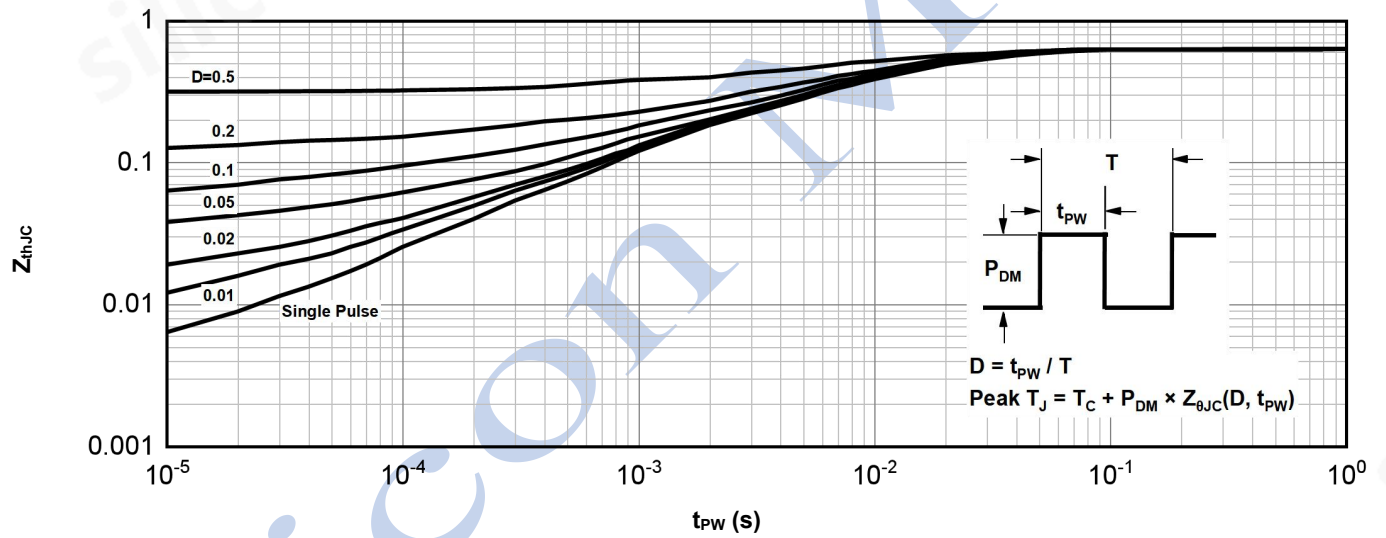
**Fig.13 Normalized  $V_{(BR)DSS}$  vs junction temperature**



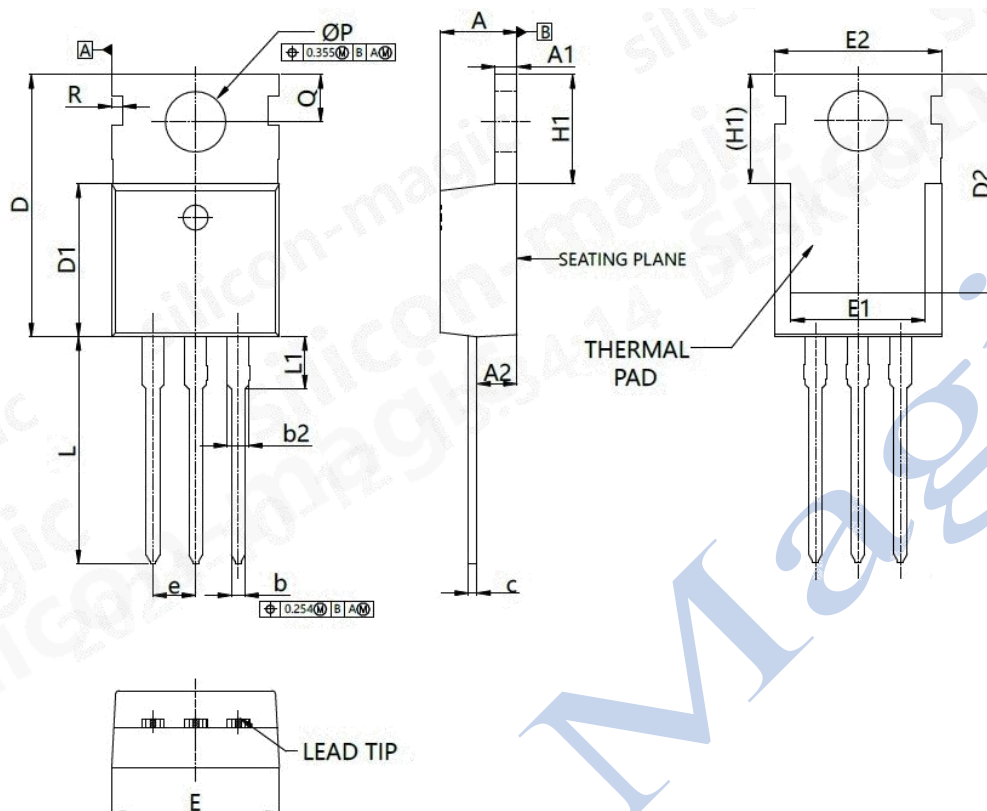
**Fig.14 Normalized  $V_{GS(th)}$  vs junction temperature**



**Fig.15 Transient thermal impedance from junction to case**



## 5. Package outline dimensions



| Dim | Millimeters |       |       |
|-----|-------------|-------|-------|
|     | Min         | Nom   | Max   |
| A   | 4.35        | 4.57  | 4.75  |
| A1  | 1.20        | 1.30  | 1.45  |
| A2  | 2.20        | 2.40  | 2.60  |
| b   | 0.70        | 0.80  | 0.90  |
| b2  | 1.17        | 1.37  | 1.52  |
| c   | 0.40        | 0.50  | 0.65  |
| D   | 15.10       | 15.60 | 16.10 |
| D1  | 8.80        | 9.20  | 9.40  |
| D2  | 12.00       | 13.00 | 13.50 |
| E   | 9.80        | 10.00 | 10.20 |
| E1  | 7.00        | 8.00  | 8.46  |
| E2  | 9.70        | 10.00 | 10.30 |
| e   | 2.44        | 2.54  | 2.64  |
| H1  | 6.25        | 6.50  | 6.85  |
| L   | 12.80       | 13.50 | 13.80 |
| L1  | 2.75        | 3.45  | 3.95  |
| P   | 3.40        | 3.60  | 3.80  |
| Q   | 2.60        | 2.85  | 3.10  |
| R   | 0.50        | 0.65  | 0.80  |

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