

Preliminary Specification: Subject to Change without Notice

N-Channel 90V MOSFET

Product summary

V_{DS} (V)	$R_{DS(on),max}$ (m Ω)	I_D (A)
90	1.3 @ $V_{GS} = 10V$	327 ⁽¹⁾

Features

- Low $R_{DS(on)}$ SGT technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

Applications

- DC/DC conversion
- Power switch
- Motor drives

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Package and ordering information

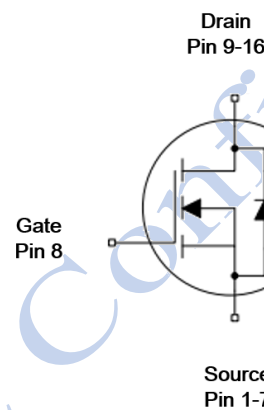
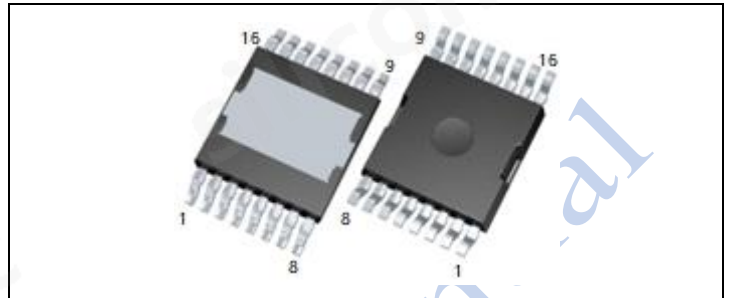
Ordering code	Package	Device code
SDH08N1P3QN-AA	TOLT	H08N1P3QN-AA

1. Maximum ratings

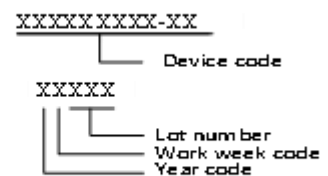
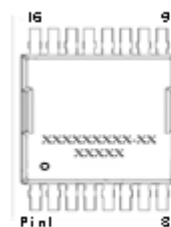
Absolute maximum ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source voltage		V_{DS}	90	V
Gate-source voltage		V_{GS}	±20	
Continuous drain current	$T_C=25^{\circ}\text{C}^{(1)}$	I_D	327	A
	$T_C=100^{\circ}\text{C}^{(1)}$		207	
	$T_A=25^{\circ}\text{C}^{(4)}$		32	
Pulsed drain current ⁽²⁾		$I_{D,pulse}$	1220	
Avalanche energy, single pulse ⁽³⁾		E_{AS}	1280	mJ
Power dissipation	$T_C=25^{\circ}\text{C}$	P_D	260	W
	$T_A=25^{\circ}\text{C}^{(4)}$		2.5	
Operating junction and storage temperature range		T_J, T_{stg}	-55 to 175	°C

TOLT



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2. Thermal resistance ratings

Thermal resistance ratings					
Parameter		Symbol	Max.	Unit	
Thermal resistance, junction-to-case	Steady state	$R_{\theta JC}$	0.5	°C/W	
Thermal resistance, junction-to-ambient ⁽⁴⁾	Steady state	$R_{\theta JA}$	65		

3. Electrical Characteristics

Electrical characteristics (T _J = 25°C unless otherwise noted)						
Parameter	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Static parameter						
Drain to source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0, I _D = 250 μA	90			V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2.3	3.1	3.9	V
Gate-body leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA
Zero gate voltage drain current	I _{DSS}	V _{DS} = 90 V, V _{GS} = 0 V			1	μA
Drain-source on-resistance	R _{DS(on)}	V _{GS} = 10 V, I _D = 100 A		1.08	1.3	mΩ
Forward transconductance ⁽⁵⁾	g _{fs}	V _{DS} = 5 V, I _D = 50 A		155		S
Gate resistance	R _g	f = 1 MHz		2.3		Ω
Dynamic ⁽⁵⁾						
Total gate charge	Q _g	V _{DS} = 45 V, I _D = 100 A, V _{GS} = 10 V		141		nC
Gate-source charge	Q _{gs}			55		
Gate-drain charge	Q _{gd}			27		
Turn-on delay time	t _{d(on)}	V _{DS} = 45V, I _D = 100 A, V _{GS} = 10 V, R _{GEN} = 6 Ω		115		ns
Rise time	t _r			80		
Turn-off delay time	t _{d(off)}			78		
Fall time	t _f			49		
Input capacitance	C _{iss}	V _{DS} = 45 V, V _{GS} = 0 V, f = 1 MHz		10572		pF
Output capacitance	C _{oss}			3117		
Reverse transfer capacitance	C _{rss}			54		
Reverse Diode Characteristics ⁽⁵⁾						
Diode forward voltage	V _{SD}	V _{GS} = 0 V, I _F = 100 A		0.9	1.2	V
Reverse recovery time	t _{rr}	V _{DS} = 45 V, I _F = 50 A, di/dt = 100 A/μs		89		ns
Reverse recovery charge	Q _{rr}			178		nC

Notes

- (1) Limited by maximum junction temperature.
- (2) Pulse width limited by maximum junction temperature.
- (3) $V_{DS} = 40 \text{ V}, V_{GS} = 10 \text{ V}, L = 0.5 \text{ mH}$.
- (4) $R_{\theta JA}$ is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5x1.5 in. board of FR-4 material.
- (5) Guaranteed by design, not subject to production testing.

4. Electrical characteristics diagrams

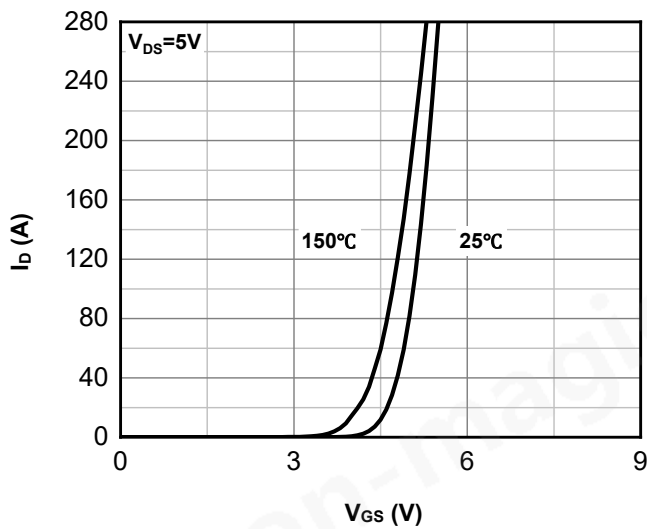
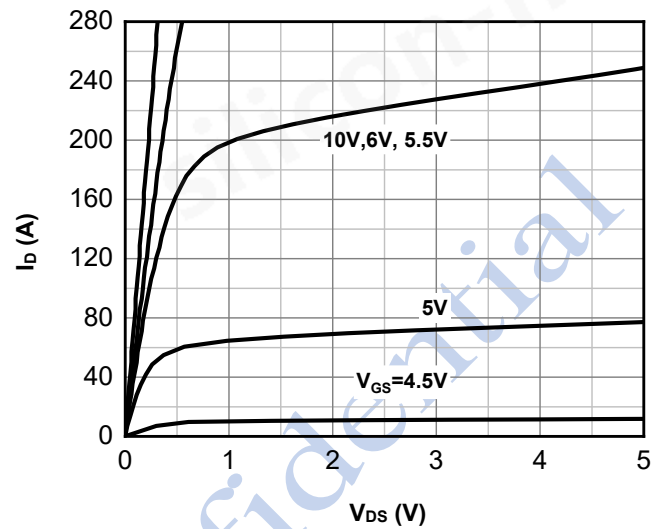
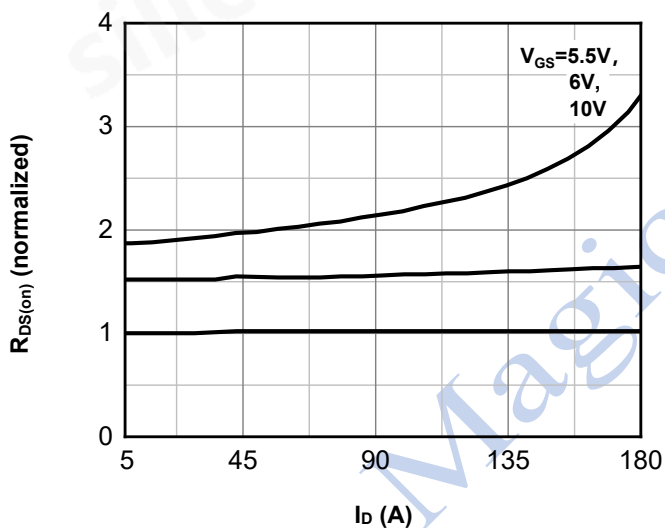
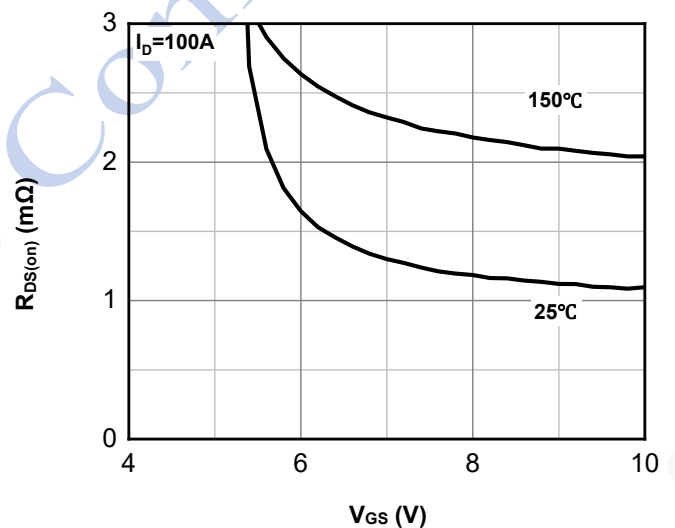
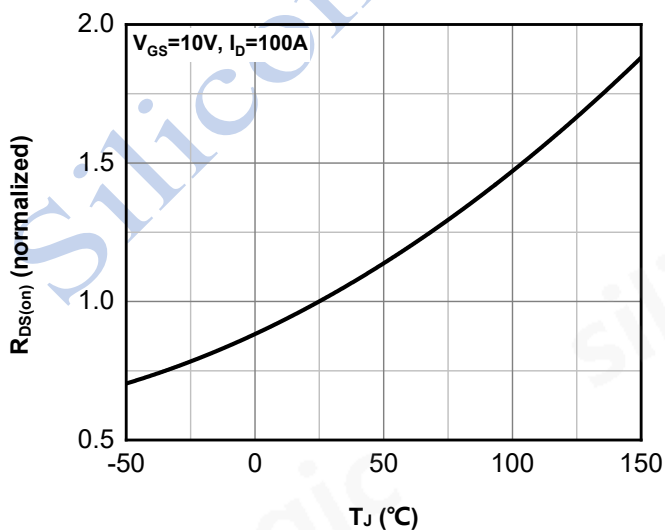
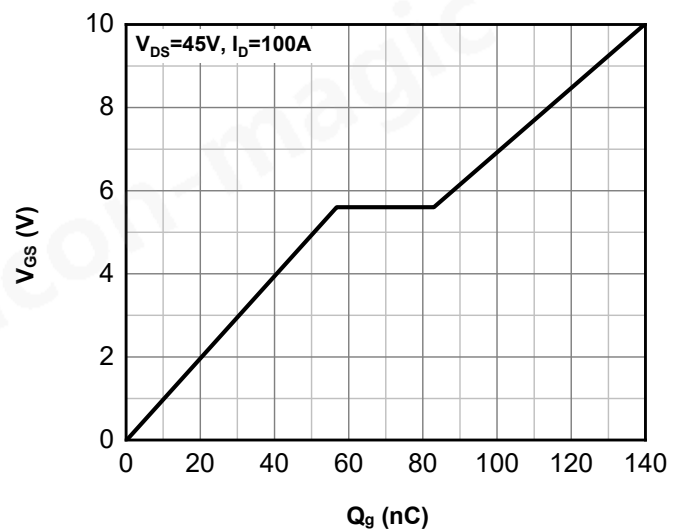
Fig.1 Typ. transfer characteristics

Fig.2 Typ. output characteristics

Fig.3 Normalized on-resistance vs drain current

Fig.4 Typ. on-resistance vs gate-source voltage

Fig.5 Normalized on-resistance vs junction temperature

Fig.6 Typ. gate charge


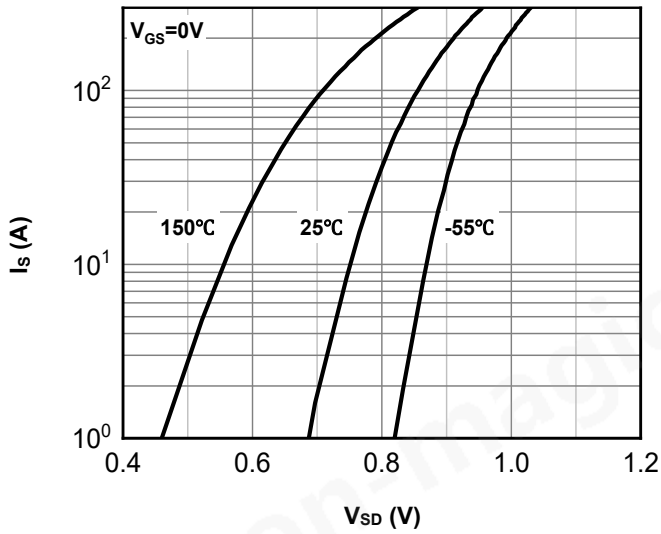
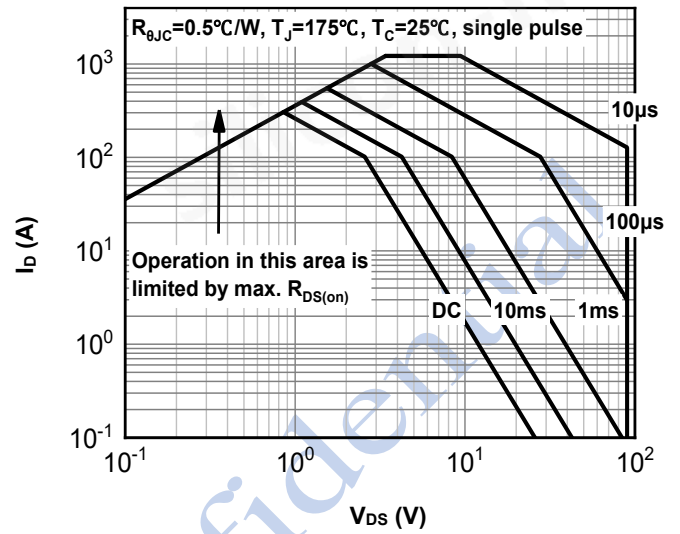
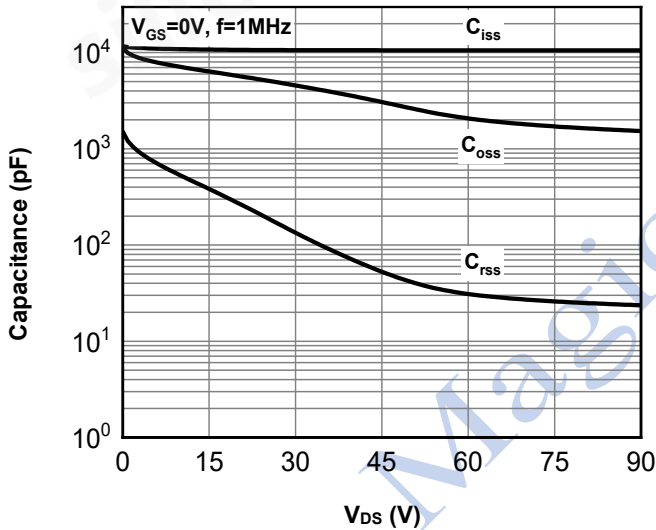
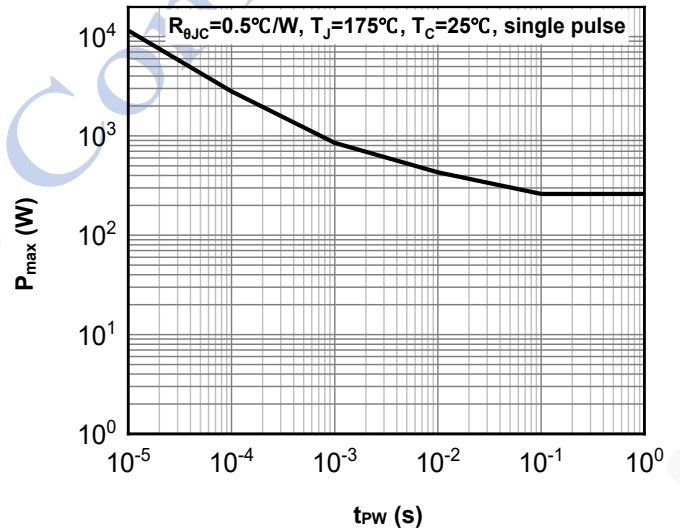
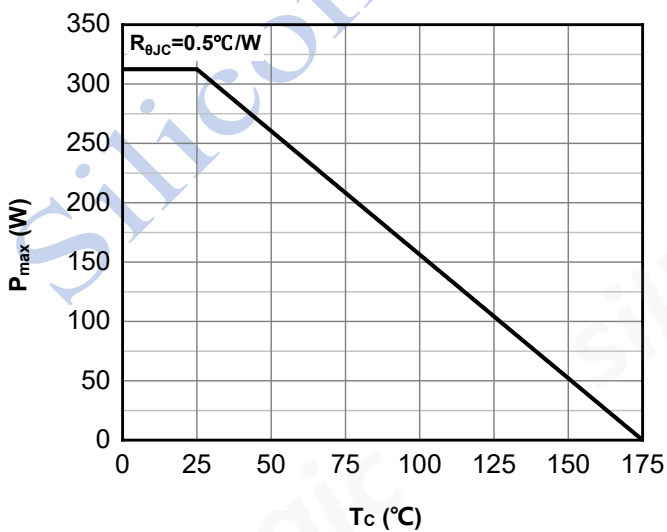
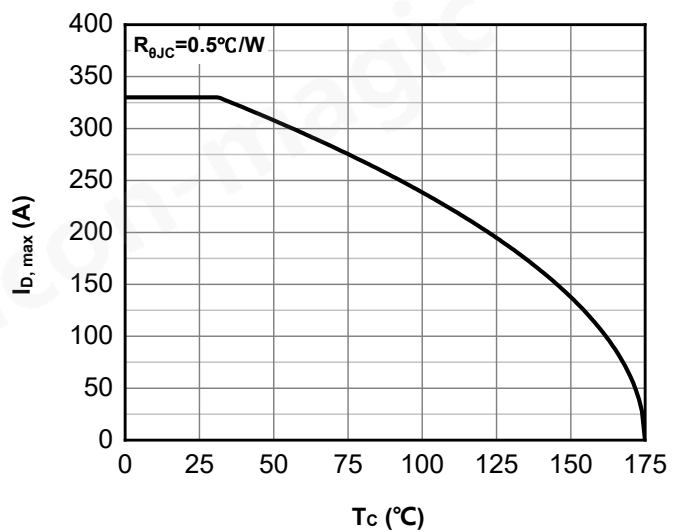
Fig.7 Typ. forward characteristics of body diode

Fig.8 Safe operating area

Fig.9 Typ. Capacitance

Fig.10 Single pulse maximum power dissipation

Fig.11 Max. power dissipation vs case temperature

Fig.12 Max. continuous drain current vs case temperature


Fig.13 Normalized $V_{(BR)DSS}$ vs junction temperature

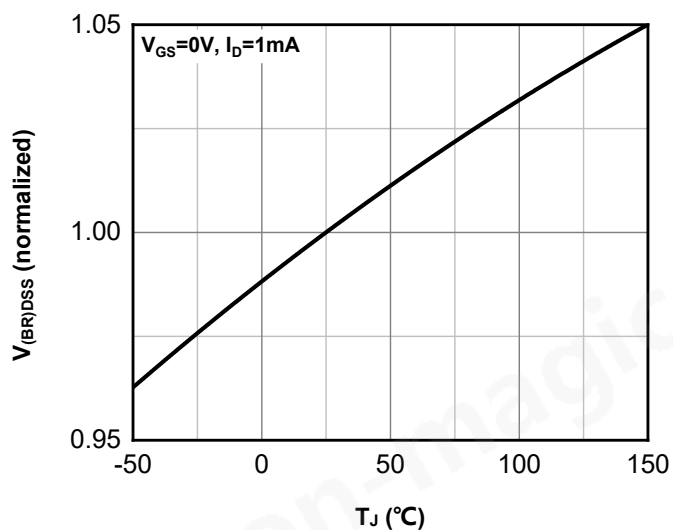


Fig.14 Normalized $V_{GS(th)}$ vs junction temperature

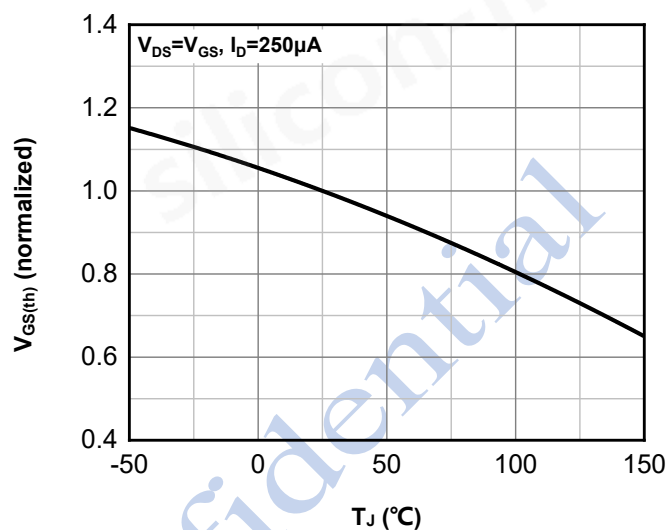
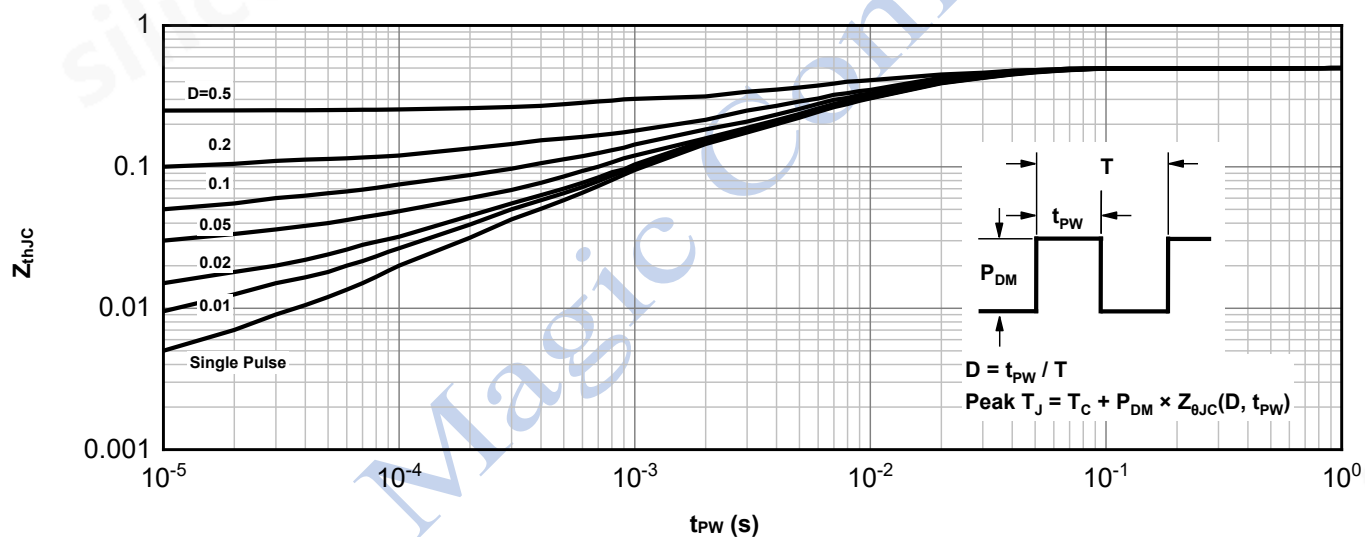
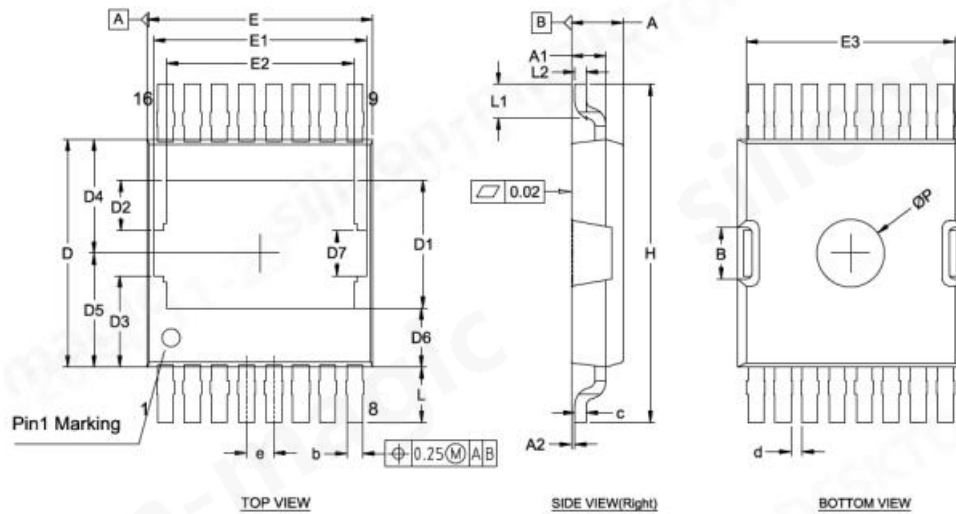


Fig.15 Transient thermal impedance from junction to case



5. Package outline dimensions



Dim	Millimeters		
	Min	Nom	Max
A	2.250	2.300	2.350
A1	1.440	1.540	1.640
A2	0.010	—	0.160
b	0.600	0.700	0.800
c	0.400	0.500	0.600
d	0.400	0.500	0.600
e	1.200 BSC		
D	10.000	10.100	10.300
D1	5.470	5.670	5.870
D2	2.040	2.240	2.440
D3	4.050 REF		
D4	5.050 REF		
D5	5.050 REF		
D6	2.620 REF		
D7	2.000 REF		
E	9.700	10.000	10.100
E1	9.460 REF		
E2	8.100	8.300	8.500
E3	9.070	9.270	9.470
H	14.800	15.000	15.200
L	2.250	2.450	2.650
L1	1.350	1.500	1.650
L2	0.500 BSC		
P	2.900	3.000	3.100
B	2.180	2.280	2.380

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